

Electronic Acknowledgement Receipt

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| EFS ID: | 1052661 |
| Application Number: | 10771630 |
| Confirmation Number: | 4117 |
| Title of Invention: | Pipelined microprocessor, apparatus, and method for generating early instruction results |
| | |
| First Named Inventor: | Gerard M. Col |
| Customer Number: | 23669 |
| Filer: | James Weldon Huffman/Taysie Locke |
| Filer Authorized By: | James Weldon Huffman |
| Attorney Docket Number: | CNTR.2095 |
| Receipt Date: | 19-MAY-2006 |
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| Time Stamp: | 17:06:30 |
| Application Type: | Utility |
| International Application Number: | |

Payment information:

| | |
|--|--------|
| Submitted with Payment | yes |
| Payment was successfully received in RAM | \$ 180 |
| RAM confirmation Number | 235 |
| Deposit Account | |

File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part | Pages |
|-----------------|----------------------|-----------|------------------|------------|-------|
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|--|--|--|--------|----|---|
| 1 | Information Disclosure Statement (IDS) Filed | IDS_CNTR_2095.pdf | 368041 | no | 2 |
| Warnings: | | | | | |
| Information: | | | | | |
| This is not an USPTO supplied IDS fillable form | | | | | |
| 2 | NPL Documents | IA_32_Intel_Arc_Software_Development_Man_Vol_3.pdf | 303096 | no | 6 |
| Warnings: | | | | | |
| Information: | | | | | |
| 3 | NPL Documents | IA_32_Intel_Arc_Software_Development_Man_Vol_2.pdf | 235880 | no | 6 |
| Warnings: | | | | | |
| Information: | | | | | |
| 4 | Fee Worksheet (PTO-875) | fee-info.pdf | 8203 | no | 2 |
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| Information: | | | | | |
| Total Files Size (in bytes): | | | 915220 | | |
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